This form is a summary description of the model entitled "ASLink" proposed for the Model Checking Contest @ Petri Nets. Models can be given in several instances parameterized by scaling parameters. Colored nets can be accompanied by one or many equivalent, unfolded P/T nets. Models are given together with property files (possibly, one per model instance) giving a set of properties to be checked on the model.

## Description

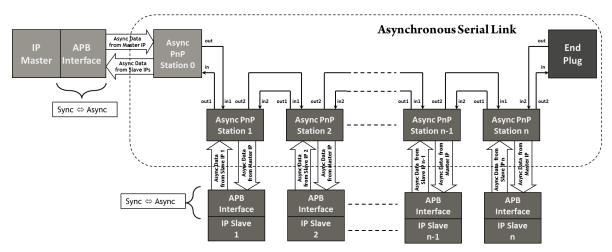
This model describes a hardware circuit component designed at Tiempo Secure <sup>(a)</sup>, namely, an on-chip asynchronous serial link, the architecture of which is illustrated in the Figure below. This link enables the interconnection of a set of IP <sup>(b)</sup> slaves designed by other vendors. Each IP slave is synchronous, must be reused as such, and can be accessed through a standard interface such as ARM's AMBA-APB <sup>(c)</sup>.

The asynchronous serial link establishes a bridge between, on the one hand, the set of IP slaves, which are daisy-chained, and, on the other hand, a synchronous master IP. It is easily configurable to fit any number of IP slaves and locations. A key feature of the link is its design based on asynchronous logics [1][2], which brings physical and logical reliability (due to the delay-insensitive implementation) and easy integration in SoCs <sup>(d)</sup> with multiple power and clock domains (because of the variability tolerance, the absence of clock, and the low wire count of the asynchronous implementation).

Following the modelling and verification methodology [3] adopted at Tiempo Secure, the asynchronous serial link was formally specified using the LNT language supported by the verification tools available in the CADP toolbox. The considered LNT specification covers the architectural part delimited by the dashed line in the Figure below. A scalable family of ten LNT specifications was made by increasing from one to ten the number of IP slaves connected to the asynchronous link.

Each LNT specification was translated automatically to LOTOS, and then to an interpreted Petri net using the CADP toolbox. Finally, a P/T net was obtained by stripping out all data-related information (variables, types, assignments, guards, etc.) from the interpreted Petri net, leading to a NUPN (Nested-Unit Petri Net) model translated to PNML using the CÆSAR.BDD tool.

Each instance of the model is parameterized by the number N of IP slaves, and also by its version V, which specifies how the NUPN has been produced from the LOTOS specification. V is either equal to "a" if the NUPN has been generated after applying all the structural and data-flow optimizations of the CÆSAR compiler for LOTOS, or to "b" if the NUPN has been generated before these optimizations.



Architecture of the Asynchronous Serial Link

<sup>(</sup>a)http://www.tiempo-secure.com

<sup>(</sup>b) see http://en.wikipedia.org/wiki/Semiconductor\_intellectual\_property\_core

<sup>(</sup>c) see http://en.wikipedia.org/wiki/Advanced\_Microcontroller\_Bus\_Architecture

 $<sup>^{</sup>m (d)}$ see http://en.wikipedia.org/wiki/System\_on\_a\_chip

#### References

[1] Marc Renaudin. Asynchronous Circuits and Systems: A Promising Design Alternative. In Microelectronics for Telecommunications: Managing High Complexity and Mobility (MIGAS'2000), special issue of the Microelectronics Engineering Journal, vol. 54, num. 1–2, Elsevier, Dec. 2000.

[2] Jens Sparsø. Asynchronous Circuit Design: A Tutorial. Technical University of Denmark, March 2006. Available from http://www.imm.dtu.dk/~jsp.

[3] Aymane Bouzafour, Marc Renaudin, Hubert Garavel, Radu Mateescu, and Wendelin Serwe. *Model-checking Synthesizable SystemVerilog Descriptions of Asynchronous Circuits*. In Proceedings of the 24th IEEE International Symposium on Asynchronous Circuits and Systems (ASYNC'18), Vienna, Austria, May 2018.

# Scaling parameter

Parameter name Parameter description		Chosen parameter values		
N	N is the number of IP slaves	from 1 to 10		

#### Size of the model

Parameter	Number of	Number of	Number of	Number of	HWB code
	places	transitions	arcs	${f units}$	
N = 01, V = a	431	735	2801	83	4-75-196
N = 01, V = b	846	1148	3624	105	12-75-284
N = 02, V = a	626	1008	3820	118	4-104-281
N = 02, V = b	1242	1621	5041	153	13-104-409
N = 03, V = a	821	1281	4839	153	4-133-366
N = 03, V = b	1638	2094	6458	201	14-133-534
N = 04, V = a	1016	1554	5858	188	4-162-451
N = 04, V = b	2034	2567	7875	249	15-162-659
N = 05, V = a	1211	1827	6877	223	4-191-536
N = 05, V = b	2430	3040	9292	297	16-191-784
N = 06, V = a	1406	2100	7896	258	4-220-621
N = 06, V = b	2826	3513	10709	345	17-220-909
N = 07, V = a	1601	2373	8915	293	4-249-706
N = 07, V = b	3222	3986	12126	393	18-249-1034
N = 08, V = a	1796	2646	9934	328	4-278-791
N = 08, V = b	3618	4459	13543	441	19-278-1159
N = 09, V = a	1991	2919	10953	363	4-307-876
N = 09, V = b	4014	4932	14960	489	20-307-1284
N = 10, V = a	2186	3192	11972	398	4-336-961
N = 10, V = b	4410	5405	16377	537	21-336-1409

## Structural properties

ordinary — all arcs have multiplicity one	
simple free choice — all transitions sharing a common input place have no other input place	)
extended free choice — all transitions sharing a common input place have the same input places	)
state machine — every transition has exactly one input place and exactly one output place	)
marked graph — every place has exactly one input transition and exactly one output transition	)
connected — there is an undirected path between every two nodes (places or transitions) ✓ (i	)

<sup>(</sup>e) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>f) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>g) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>h) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>i) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

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strongly connected — there is a directed path between every two nodes (places or transitions)	<b>X</b> (j)	
source place(s) — one or more places have no input transitions		
sink place(s) — one or more places have no output transitions		
source transition(s) — one or more transitions have no input places		
sink transitions(s) — one or more transitions have no output places		
loop-free — no transition has an input place that is also an output place		
conservative — for each transition, the number of input arcs equals the number of output arcs	<b>X</b> (p)	
subconservative — for each transition, the number of input arcs equals or exceeds the number of output arcs	<b>X</b> (q)	
nested units — places are structured into hierarchically nested sequential units (r)		

## Behavioural properties

safe — in every reachable marking, there is no more than one token on a place	<b>✓</b> (s)
dead place(s) — one or more places have no token in any reachable marking	
dead transition(s) — one or more transitions cannot fire from any reachable marking	
deadlock — there exists a reachable marking from which no transition can be fired	
reversible — from every reachable marking, there is a transition path going back to the initial marking	?
live — for every transition t, from every reachable marking, one can reach a marking in which t can fire	

## Size of the marking graphs

D. 4	Number of reach-	Number of tran-	Max. number of	Max. number of
Parameter	able markings	sition firings	tokens per place	tokens per marking
N = 01, V = a	$\geq 8.74114e + 07^{(t)}$	?	1 <sup>(u)</sup>	$\in [21, 75]^{(v)}$
N = 01, V = b	$\geq 2.38488e + 11^{\text{(w)}}$	?	1 <sup>(x)</sup>	$\in [19, 75]^{(y)}$
N = 02, V = a	$\geq 1.43471e + 09^{\text{(z)}}$	?	1 <sup>(aa)</sup>	$\in [31, 104]^{\text{(ab)}}$
N = 02, V = b	$\geq 5.69187e + 14^{\text{(ac)}}$	?	1 <sup>(ad)</sup>	$\in [29, 104]^{\text{(ae)}}$
N = 03, V = a	$\geq 7.51327e + 10^{\text{(af)}}$	?	1 (ag)	$\in [41, 133]^{\text{(ah)}}$
N = 03, V = b	$\geq 1.24479e + 22^{\text{(ai)}}$	?	1 <sup>(aj)</sup>	$\in [39, 133]^{\text{(ak)}}$
N = 04, V = a	$\geq 2.88156e + 11^{\text{(al)}}$	?	1 (am)	$\in [51, 162]^{\text{(an)}}$
N = 04, V = b	$\geq 4.86333e + 27^{\text{(ao)}}$	?	1 (ap)	$\in [49, 162]^{\text{(aq)}}$
N = 05, V = a	$\geq 1.67215e + 08^{\text{(ar)}}$	?	1 (as)	$\in [61, 191]^{\text{(at)}}$
N = 05, V = b	$\geq 1.27462e + 33^{\text{(au)}}$	?	1 (av)	$\in [59, 191]^{\text{(aw)}}$
N = 06, V = a	$\geq 2.08612e + 20^{\text{(ax)}}$	?	1 (ay)	$\in [71, 220]^{\text{(az)}}$
N = 06, V = b	$\geq 3.43882e + 38^{\text{(ba)}}$	?	1 <sup>(bb)</sup>	$\in [69, 220]^{\text{(bc)}}$
N = 07, V = a	$\geq 2.00858e + 23^{\text{(bd)}}$	?	1 <sup>(be)</sup>	$\in [81, 249]^{\text{(bf)}}$
N = 07, V = b	$\geq 2.06918e + 44^{\text{(bg)}}$	?	1 (bh)	$\in [79, 249]^{\text{(bi)}}$
N = 08, V = a	$\geq 1.25256e + 26^{\text{(bj)}}$	?	1 <sup>(bk)</sup>	$\in [91, 278]^{\text{(bl)}}$
N = 08, V = b	$\geq 1.3975e + 49^{\text{(bm)}}$	?	1 <sup>(bn)</sup>	$\in [89, 278]^{\text{(bo)}}$
N = 09, V = a	$\geq 1.18847e + 29^{\text{(bp)}}$	?	1 <sup>(bq)</sup>	$\in [101, 307]^{\text{(br)}}$
N = 09, V = b	$\geq 6.32435e + 54^{\text{(bs)}}$	?	1 (bt)	∈ [99, 307] <sup>(bu)</sup>
N = 10, V = a	$\geq 6.65672e + 31^{\text{(bv)}}$	?	1 (bw)	$\in [111, 336]^{\text{(bx)}}$
N = 10, V = b	$\geq 1.7836e + 58^{\text{(by)}}$	?	1 <sup>(bz)</sup>	∈ [109, 336] (ca)

 $<sup>^{(</sup>j)}$  from place 1 one cannot reach place 0.

<sup>(</sup>k) place 0 is a source place.

<sup>(1)</sup> stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>m) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>n) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>o) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>p) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>q) stated by CÆSAR.BDD version 2.7 on all 20 instances (10 values of N).

<sup>(</sup>r) the definition of Nested-Unit Petri Nets (NUPN) is available from http://mcc.lip6.fr/nupn.php

<sup>(</sup>s) safe by construction – stated by the CÆSAR compiler.

<sup>(</sup>t) stated by CÆSAR.BDD version 2.7.

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(u) stated by the CÆSAR compiler.
(v) upper bound given by the number of leaf units.
(w) stated by CÆSAR.BDD version 2.7.
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- (x) stated by the CÆSAR compiler.
- (y) upper bound given by the number of leaf units.
- (z) stated by CÆSAR.BDD version 2.7.
- (aa) stated by the CÆSAR compiler.
- (ab) upper bound given by the number of leaf units.
- (ac) stated by CÆSAR.BDD version 2.7.
- (ad) stated by the CÆSAR compiler.
- $^{\rm (ae)}$  upper bound given by the number of leaf units.
- (af) stated by CÆSAR.BDD version 2.7.
- (ag) stated by the CÆSAR compiler.
- (ah) upper bound given by the number of leaf units.
- (ai) stated by CÆSAR.BDD version 2.7.
- (aj) stated by the CÆSAR compiler.
- (ak) upper bound given by the number of leaf units.
- (al) stated by CÆSAR.BDD version 2.7.
- (am) stated by the CÆSAR compiler.
- (an) upper bound given by the number of leaf units.
- (ao) stated by CÆSAR.BDD version 2.7.
- (ap) stated by the CÆSAR compiler.
- (aq) upper bound given by the number of leaf units.
- (ar) stated by CÆSAR.BDD version 2.7.
- (as) stated by the CÆSAR compiler.
- (at) upper bound given by the number of leaf units.
- (au) stated by CÆSAR.BDD version 2.7.
- (av) stated by the CÆSAR compiler.
- $^{(aw)}$  upper bound given by the number of leaf units.
- (ax) stated by CÆSAR.BDD version 2.7.
- (ay) stated by the CÆSAR compiler.
- $^{\rm (az)}$  upper bound given by the number of leaf units.
- (ba) stated by CÆSAR.BDD version 2.7.
- (bb) stated by the CÆSAR compiler.
- (bc) upper bound given by the number of leaf units.
- (bd) stated by CÆSAR.BDD version 2.7.
- (be) stated by the CÆSAR compiler.
- (bf) upper bound given by the number of leaf units.
- (bg) stated by CÆSAR.BDD version 3.3.
- $^{\rm (bh)}$  stated by the CÆSAR compiler.
- (bi) upper bound given by the number of leaf units.
- (bj) stated by CÆSAR.BDD version 2.7.
- (bk) stated by the CÆSAR compiler.
- (bl) upper bound given by the number of leaf units.
- (bm) stated by CÆSAR.BDD version 3.3.
- (bn) stated by the CÆSAR compiler.
- (bo) upper bound given by the number of leaf units.
- (bp) stated by CÆSAR.BDD version 2.7.
- (bq) stated by the CÆSAR compiler.
- $^{\rm (br)}$  upper bound given by the number of leaf units.
- (bs) stated by CÆSAR.BDD version 3.3.
- (bt) stated by the CÆSAR compiler.
- (bu) upper bound given by the number of leaf units.
- (bv) stated by CÆSAR.BDD version 3.3.
- $^{\rm (bw)}$  stated by the CÆSAR compiler.
- (bx) upper bound given by the number of leaf units.
- (by) stated by CÆSAR.BDD version 3.3.

Model: ASLink Type: P/T Net Origin: Industrial (Tiempo Secure)

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<sup>(</sup>ca) upper bound given by the number of leaf units.